

AUG-22-2008 TUE 04:32 PM TDG EXECUTIVE

FAX NO. 97747499

P. 02

Attorney Docket No. 039153-0649 (H0982)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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Applicant: Xiang et al.
Title: FULLY DEPLETED STRAINED
SEMICONDUCTOR ON
INSULATOR TRANSISTOR AND
METHOD OF MAKING THE
SAME
Appl. No.: 10/773,026
Filing Date: 02/05/2004
Examiner: Doty, Heather Anne
Art Unit: 2813
Confirmation Number: 6093

SEP 11 2006

CERTIFICATE OF EXPRESS MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is published by the Commissioner for Patents, P.O. Box 1460, Alexandria, VA 22313-1460.

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(Signature)

DECLARATION UNDER 37 C.F.R. 1.131

Commissioner for Patents and Trademarks
Washington, D.C. 20231

Sir:

We, QI XIANG, PAUL R. BESSER, MINH VAN NGO, ERIC N. PATON and

HAIHONG WANG, state and declare that:

1. We are the inventors of the invention recited in claims 11-30 of the patent application identified above. The subject matter of the patent application was invented while we were employed at Advanced Micro Devices, Inc., the Assignee of the patent application.

2. On or before June 28, 2002, we conceived in the United States the subject matter of Claims 11-30 as evidenced by the attached Exhibit A. The invention was recorded during a SMOS patent harvesting session that occurred on June 28, 2002.

3. Exhibit A (6 pages), titled "AMD INVENTION DISCLOSURE," marked "H0982," is a copy of an invention disclosure document used in the routine business practice of Advanced Micro Devices, Inc. for disclosing inventive subject matter to corporate patent counsel.

MILW_3064845.1

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4. Exhibit A, received by AMD's Tech. Law Department on June 28, 2002, discloses the subject matter of Claims 11-30.

5. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: _____

By: _____
QI XIANGDate: 8/22/06By: Paul R Besser
PAUL R. BESSER

Date: _____

By: _____
MINH VAN NGO

Date: _____

By: _____
ERIC N. PATON

Date: _____

By: _____
HAIHONG WANG